

SILABUS PILIHAN TEKNIK KONTROL

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|---------------------------------|---|--|
| Mata Kuliah                     | : | Analisis Perancangan IC  |
| Kode                            | : | E113923  |
| Silabus                         | : | Introduction to CMOS devices, manufacturing technology, CMOS inverters and gates. The course includes the understanding of CMOS design parameters and methodology such as propagation delay, noise margins, and power dissipation digital circuits with respect to different quality metrics: cost, speed, power dissipation, and reliability. The course also teaches the student to design and optimize the standard cell of combinatorial and sequential digital circuits |
| Luaran                          | : | <ol style="list-style-type: none"> <li>1. Participants understand the Digital IC methodology</li> <li>2. Participants understand the factors that affect the performance of Digital IC</li> <li>3. Participants can design combinatorial and sequential standard cell IC</li> <li>4. Participants can design a simple system.</li> </ol>   |
| Syarat/<br>PraSyarat MK<br>Lain | : | Sistem Digital   |
| Alokasi                         | : | 16 kali pertemuan  |
| Sumber Pustaka                  | : |  |